AGILENT TECHNOLOGIES, INC. Legal Department, DL428 Intellectual Property Administration P. O. Box 7599 Loveland, Colorado 80537-0599

RECEIVED CENTRAL FAX CENTEATTORNEY DOCKET NO. 10030071-01 AUG 1 1 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE											
I	nventor(s):	Tyler Sims						N OFFIC	5		
5	Serial No.: 10/712701			•		Exam	iner:	Mark C	ooff-ny C		
Filing Date: November 13, 2003				Examiner: Mruk, Geoffrey S Group Art Unit: 2853							
Title: Resistor Shapes For Heating Devices On An Integrated Circuit											
- The state of the line graved Circuit											
COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria VA 22313-1450											
s	Sir: TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT										
Transmitted herewith is/are the following in the above-identified application: Response/Amendment											
Petition to extend time to response							espond				
<u></u>	New fee as calculated below Supplemental Declaration										
No additional fee (Address envelope to "Mail Stop Amendments")											
)			
Ì	(1) FOR		AS AMENDE	D BY OTHER THAN	A SMA	LL ENT					
ļ	TOTAL	CLAIMS RÉMAINING AFTER AMENDMENT	NUMBER EXTRA	HIGHEST NUME PREVIOUSLY PAIR	ER	PRES EXT	ENT	(6) RATE	ADDITION	AL	
-	CLAIMS INDEP.	9	MINUS	20	-	= 0		X 50	FEES (
-	CLAIMS	11	MINUS	3	ì	= 0		X 200			
1	FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						+ 360 \$ 0				
	EXTENSION FEE	1 ST MONTH 120.00	2 ND MONT 450.00				4 TH MC		\$ 0		
E							1590,0 OTH	50 5 5 5 5 T	\$ (5	
Cha	arge \$ 0	to Deposit Account 5	10 4070 A	TOTAL ADDITIONAL					5 (
Charge \$ 0 to Deposit Account 50-1078. At any time during the pendency of this application, please charge any fees required or credit any over payment to Deposit Account 50-1078 pursuant to 37 CFR 1.2 5. Additionally please charge any fees to Deposit Account 50-1078 under 37 CFR 1.16, 1.17, 1.19, 1.20 and 1.21. A duplicate copy of this transmittal letter is enclosed.											
Respectfully submitted.											
Tyler Sims											
thereby certify that this paper is being fecsimile transmitted to the Patent and Trademark Office on											
transmitted to the Patent and Trademark Office on the date shown below:						Pamela Lau Kee					
Date :	of facsimile:	anach	مسم م ما		Att	omey/	Ageni	t for Appli	cant(s)		
Date of facsimile: // August 2005						Reg. No. 36,184					
/							Date: 11 August 2015				
			D	7	Te	lephon	e No	408 553	3059		

Rev 10/04 (TransAmd)

RECEIVED CENTRAL FAX CENTER

A10030071-1

AUG 1 1 2005

() I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450

(X) I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Date of Deposit: Typed Name: Signature:

Date of Facsimile: // august 2005
Typed Name: Ann Marie Raddyffe
Signature: anumane Badluffe

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Tyler Sims

Examiner: Mruk, Geoffrey S

Serial No.: 10/712,701

Group Art Unit: 2853

Filing Date: 11/13/2003

Title: Resistor Shapes For Heating Devices On An Integrated Circuit

Sir:

In an Office Action dated 28 July 2005 having a 30 day statutory period of response expiring 27 August 2005, the Examiner has asked the applicants to elect one of the following species:

Species 1: Claims 1-9, drawn to a heating device within an integrated circuit comprising: a first conductive lead; a second conductive lead; a third conductive lead; a first resistive region connected between the first conductive lead and the third conductive lead; and a second resistive region connected between the second conductive lead and the third conductive lead

Species 2: Claims 10-13, drawn to a heating device comprising: a first region formed from bottom layer of resistive material; a second region and a third region formed from the middle layer of resistive material; and a fourth region and a fifth region formed from a top layer of conductive material

Species 3: Claims 14-20, drawn to a heating device with an integrated circuit, comprising: a first conductive lead; a second conductive lead; a third conductive lead; a fourth conductive lead; a first resistive region connected between the first conductive lead and the third conductive lead; and a second resistive region connected between the second conductive lead and the fourth conductive lead

Applicants elect Species 1. The pending claims are presented herein.